

## **REMARKS**

### **Oath/Declaration**

Examiner has indicated that the declaration is defective due to illegible words in the top right of the form and has required a new declaration in compliance with 37 CFR 1.67(a). Applicant believes that the missing letters are due to USPTO scanning errors and has enclosed a true and correct copy of the Declaration as originally filed in the application which does not contain any missing information.

### **Claim Rejections – 35 U.S.C. 112**

Claims 5-8 and 14-17 are rejected under 35 U.S.C. §112, second paragraph, as being indefinite. Claim 5 and 14 have been canceled in this Response. The limitations recited in those claims have been incorporated into independent claims 1 and 10, respectively. Applicant has reworded the language incorporated into claims 1 and 10 to address the issues raised by Examiner. It is respectfully submitted, therefore, that the rejection under 35 U.S.C. §112 has been overcome.

### **Claim Rejections – 35 U.S.C. 102**

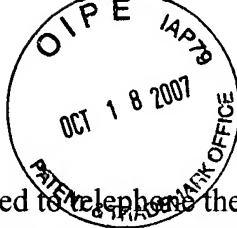
Claims 1-8 and 10-17 are rejected under 35 U.S.C. §102(a) and (e) as being anticipated by U.S. Publication No. 2003/0105828 to Sano et al. (hereinafter “Sano”). Dependent claims 2, 4, 5, 8, 11, 13, 14, and 17 have been canceled in this Response with the limitations recited in those claims now incorporated into the corresponding independent claims. Independent claims 1 and 10, as amended, recite limitations for a system and method wherein a bridge in a second data processing system is operable to perform an uncacheable remote access to a cacheable coherent memory space of a first data processing system. These claims further recite that the remote access by said bridge to perform said data write is performed in accordance with a set of predetermined ordering rules. This feature was previously recited in dependent claims 8 and 17. Examiner has failed to identify this latter feature in Sano. Applicant, therefore, respectfully submits that Independent claims 1 and 10 are allowable over the Sano reference and the rejection of these claims under 35 U.S.C. §102 (a) and (e) should be removed.

### **Claim Rejections – 35 U.S.C. 103**

Claims 1-8 and 10-17 are rejected under 35 U.S.C. §103(a) as being unpatentable over U.S. Patent No. 6,470,429 to Jones (hereinafter “Jones”) in view of U.S. Patent No. 6,134,641 to Anand (hereinafter “Anand”). In the Response to the rejection of these claims in the previous Office Action, Applicant noted that Examiner is correct in stating that Jones does not teach the bridge performing an uncacheable remote access to the cacheable coherent memory space of the first data processing system. Examiner took the position that Anand supplies a teaching of using [an] uncacheable request to access a cacheable coherent memory space, citing column 5, lines 40-45 and column 9, lines 9-12. In the prior Response, Applicant stated that Anand does not teach the use of an uncacheable request to access a cacheable coherent memory space. In the current Office Action, Examiner states that Applicant’s remarks were unpersuasive and cites column 9, lines 9-12 for the proposition that “Anand teaches a non-cacheable request to a cacheable memory space without any coherency problem....” The cited portion of Anand, however, states that “the non-cacheable block of system memory” may be addressed without any coherency problems. Applicant, therefore, submits again that Anand fails to provide the teachings missing in Jones. Applicant respectfully submits, therefore, that Examiner has failed to meet the requirements of 35 U.S.C. §103(a) in applying the combination of Jones and Anand as a basis for rejecting independent claims 1 and 10 and, therefore, the rejection of these claims should be removed. Furthermore, all remaining dependent claims are allowable as being dependent on an allowable base claim.

### **CONCLUSION**

In view of the remarks set forth herein, Applicants respectfully submit that all pending claims are in condition for allowance. Accordingly, Applicants request that the rejection of claims 1-8 and 10-16 be withdrawn and that a Notice of Allowance be issued. Nonetheless, should any issues remain that might be subject to resolution through a telephone interview, the



Examiner is requested to telephone the undersigned at 512-338-9100.

I hereby certify that this correspondence is being deposited with the United States Postal Service as First Class Mail in an envelope addressed to Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450 on October 15, 2007.

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Respectfully submitted,

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